

CLOCK DISTRIBUTION CIRCUITS AND METHODS OF OPERATING SAME
THAT USE MULTIPLE CLOCK CIRCUITS CONNECTED BY PHASE
DETECTOR CIRCUITS TO GENERATE AND SYNCHRONIZE LOCAL CLOCK
SIGNALS

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ABSTRACT OF THE DISCLOSURE

Multiple clock circuits are connected by phase detector circuits to generate and synchronize local clock signals. For example, a clock distribution circuit includes a first clock circuit that is configured to generate a first clock signal in response to a first error signal, and a second clock circuit that is configured to generate a second clock signal in response to the first error signal. A first phase detector circuit connects the first clock circuit to the second clock circuit and is configured to generate the first error signal in response to the first and the second clock signals.